

AMENDMENTS TO THE SPECIFICATION

IN THE SPECIFICATION:

Please amend the paragraph beginning on page 4, line 11, as follows:

--FIG. 3 further explains the switch circuit 21. The switch circuit 21 contains a first switch device 211 and a second switch device 212. The first switch device 211 is an MOS (Metal Oxide Semiconductor) with its first input connecting to the voltage input VI, its second input connecting to the test circuit 22, and its inputs input 11 connects to the control unit 10. Therefore, it can send the normal voltage from the voltage input VI to the control unit 10 for its operation. After receiving a switch voltage from the test circuit 22, the first switch device 211 stops the normal voltage transmission to the control unit 10. The second switch device 212 is also an MOS, with its first input connecting to the frequency division circuit 23, its second input connecting to the test circuit 22, and its inputs connecting to the control unit 10. After receiving a switch voltage from the test circuit 22, the second switch device 212 transmits the frequency division voltage from the frequency division circuit 23 to the control unit 10 for its continuous operation.--